F I G . 1

F I G . 2

F I G . 3

F I G . 4

90

F I G . 5

b 0	b 1	DECIMAL NUMBER	D A O
0	0	0	0
1	0	1	(1/4) V R E F
0	1	2	(2/4) V R E F
1	1	3	(3/4) V R E F

F I G. 6

FIG.7

FIG. 8
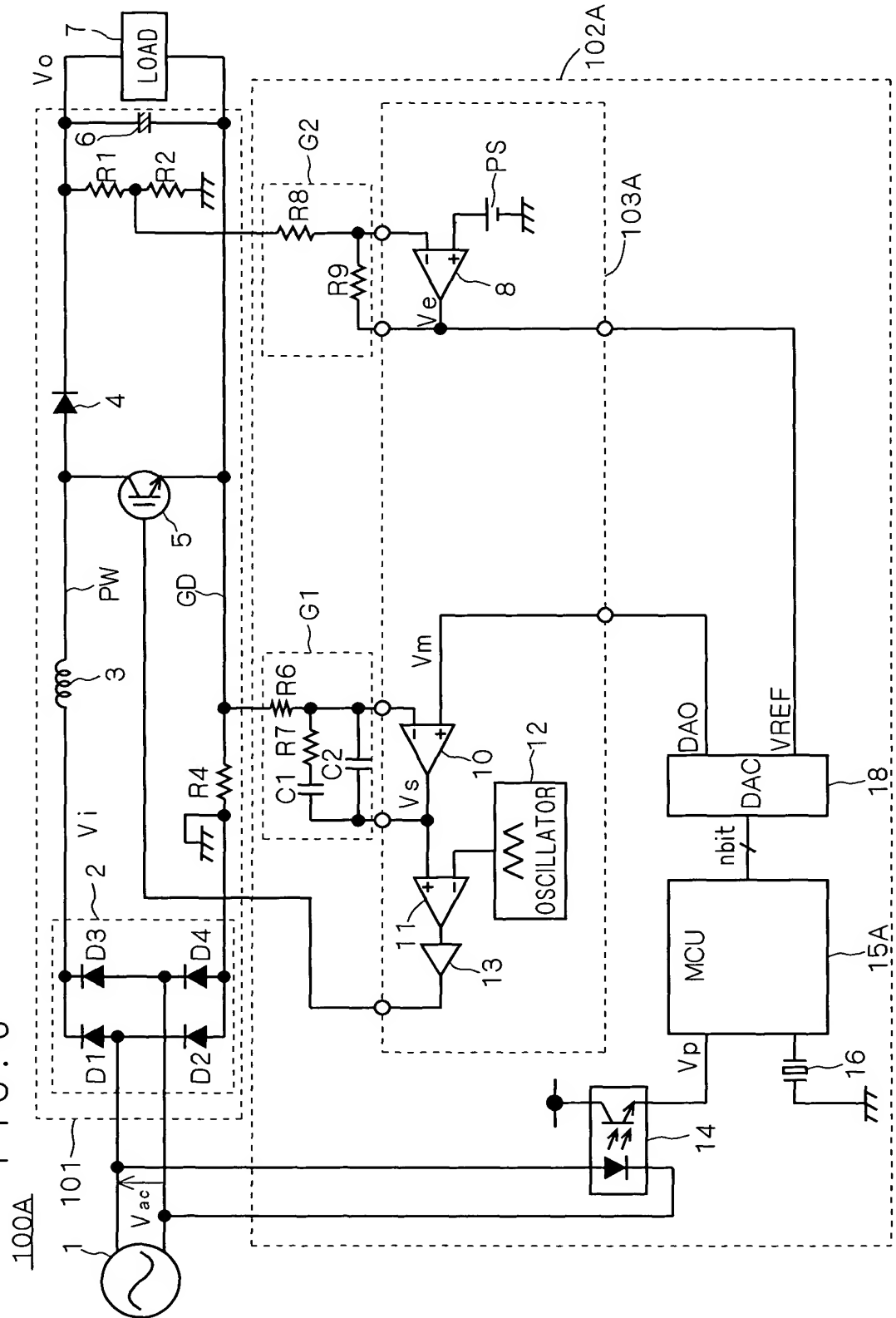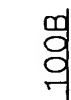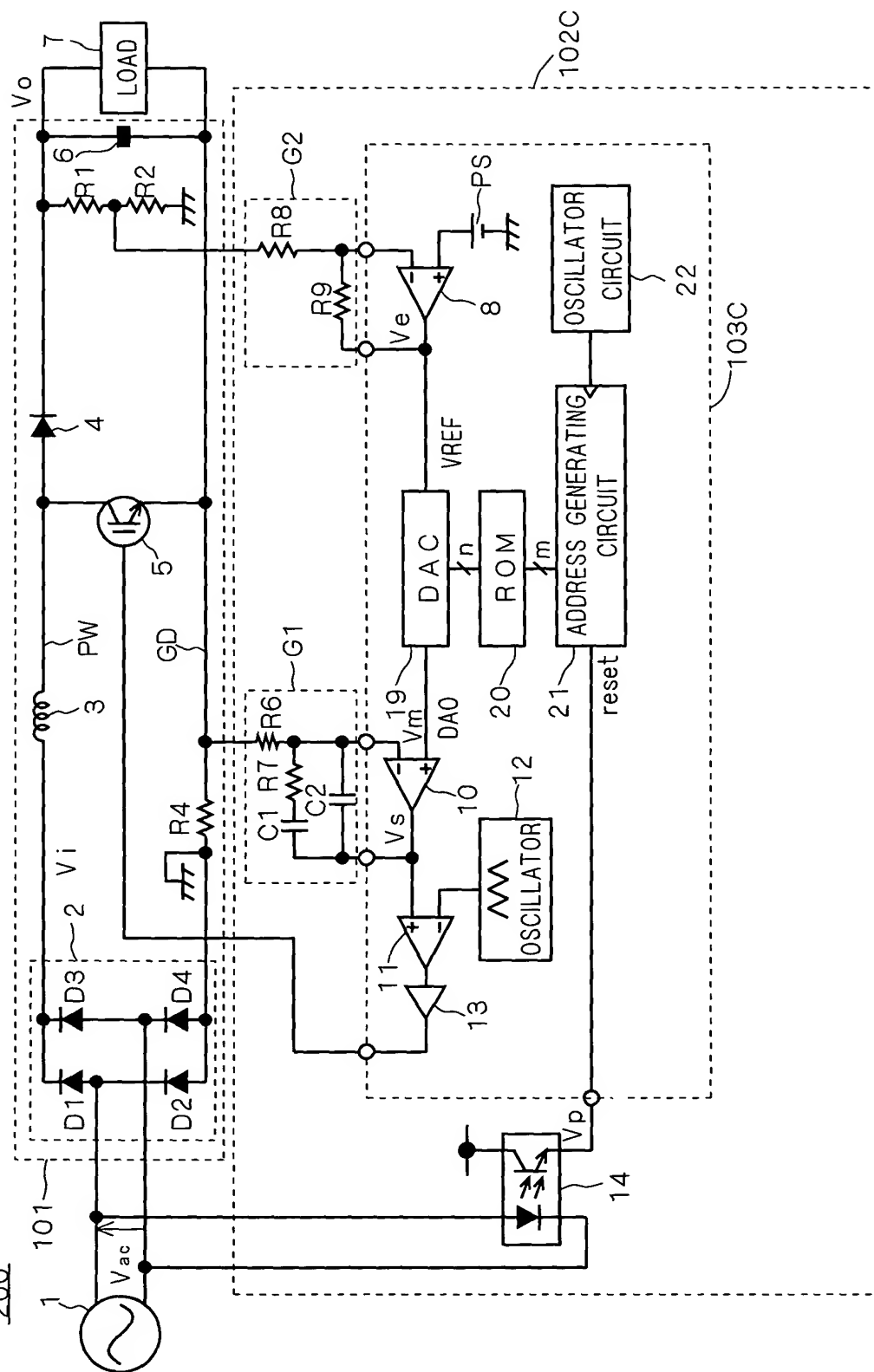
F I G . 9

F I G . 10

F I G . 1 1

F I G. 12

F I G . 1 3
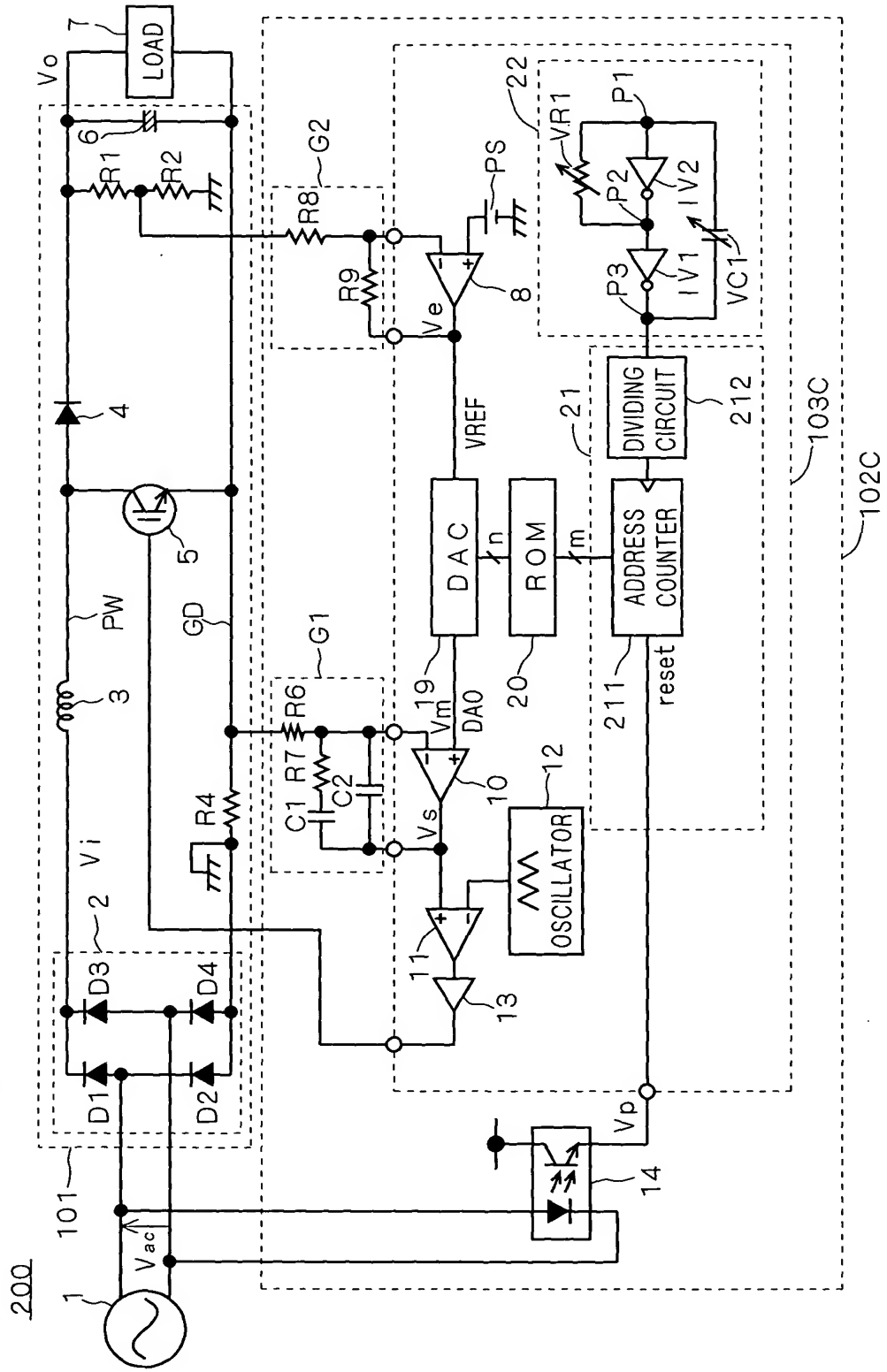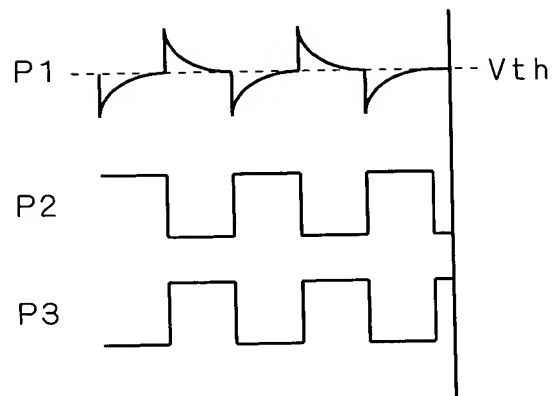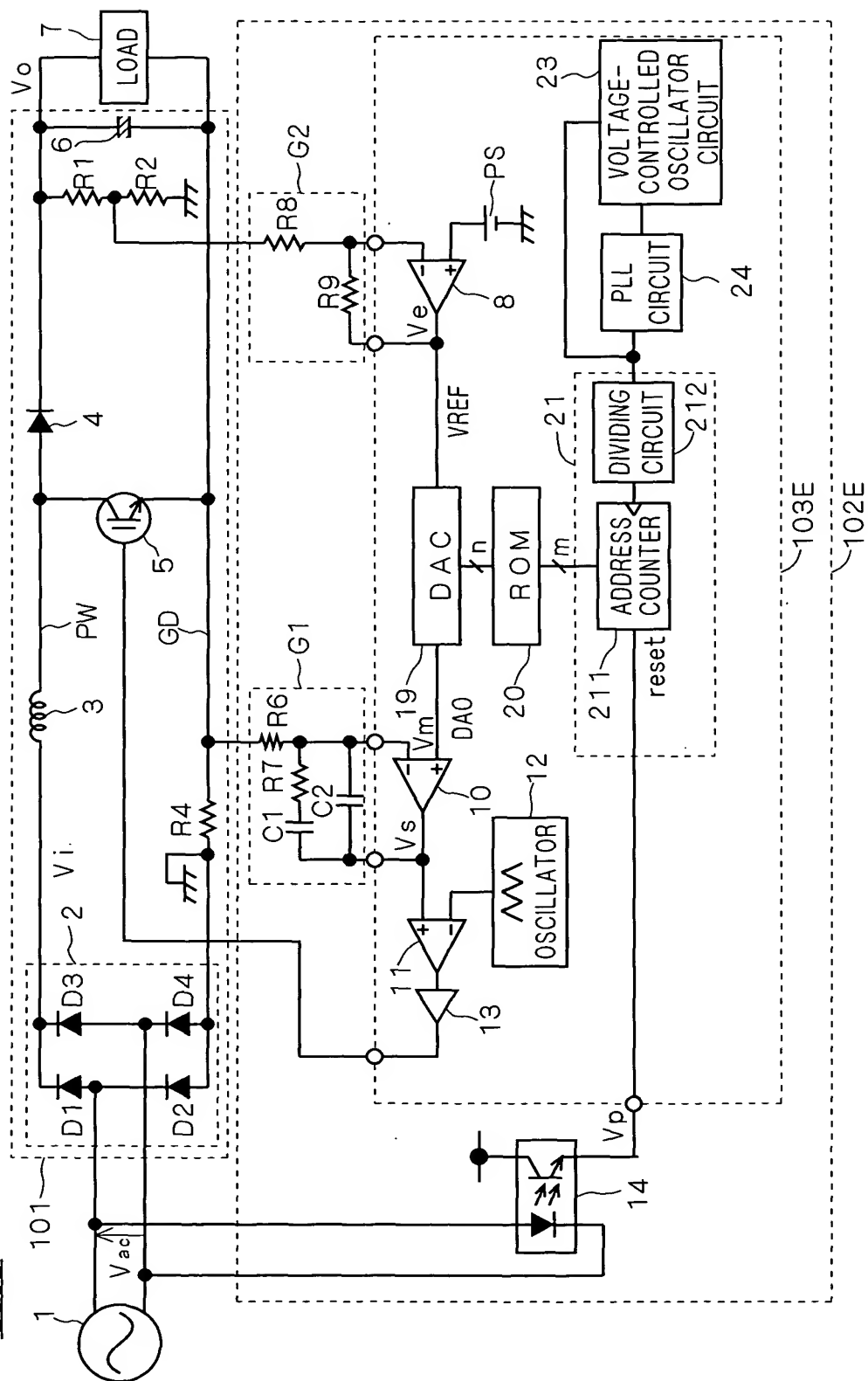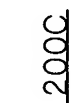
FIG. 14
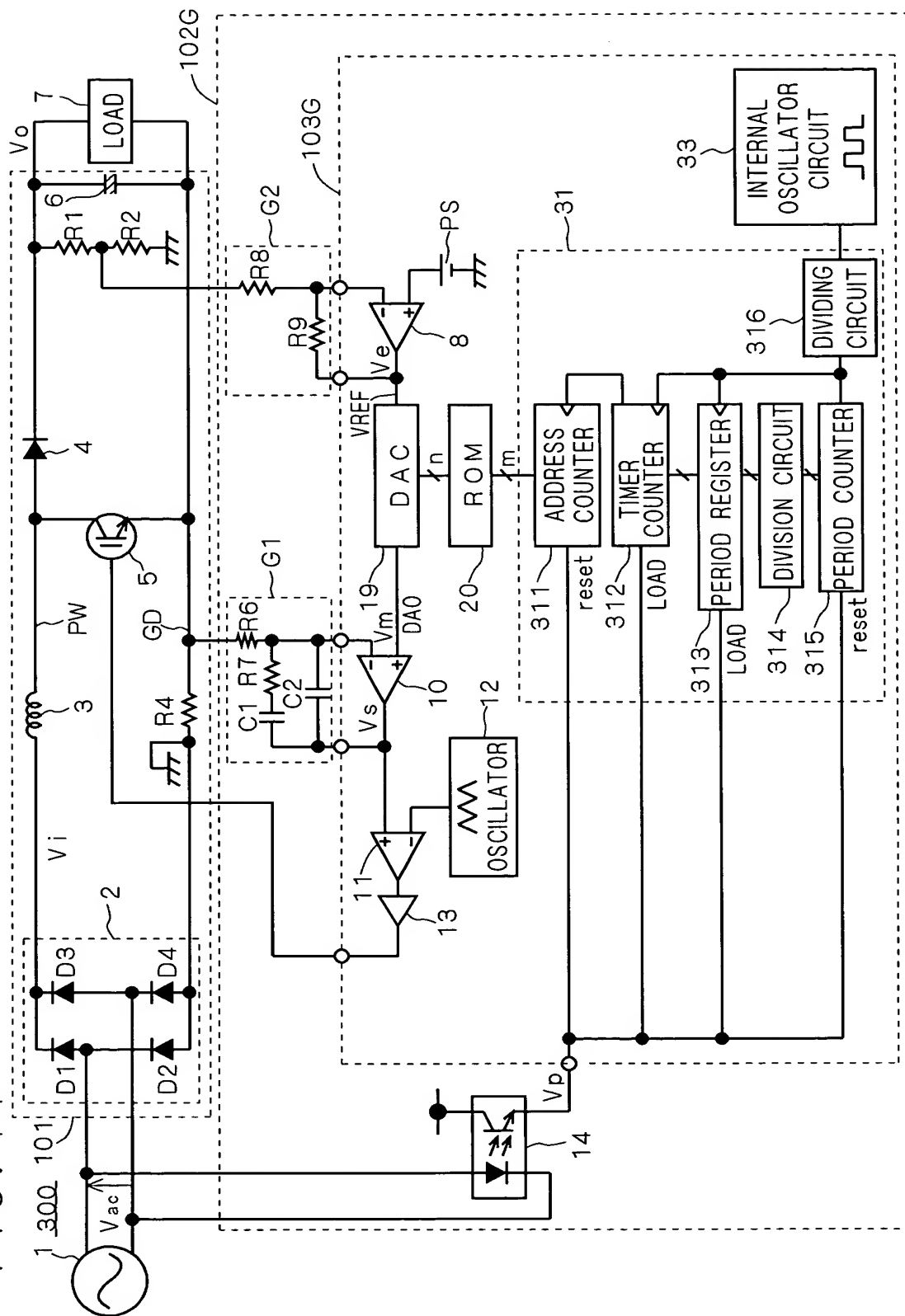
F I G . 1 5
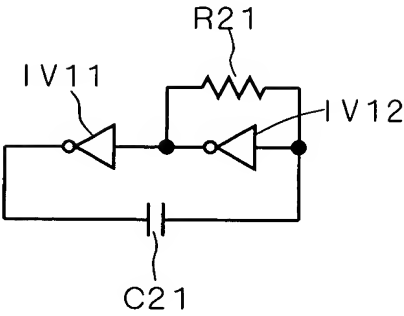
FIG. 16
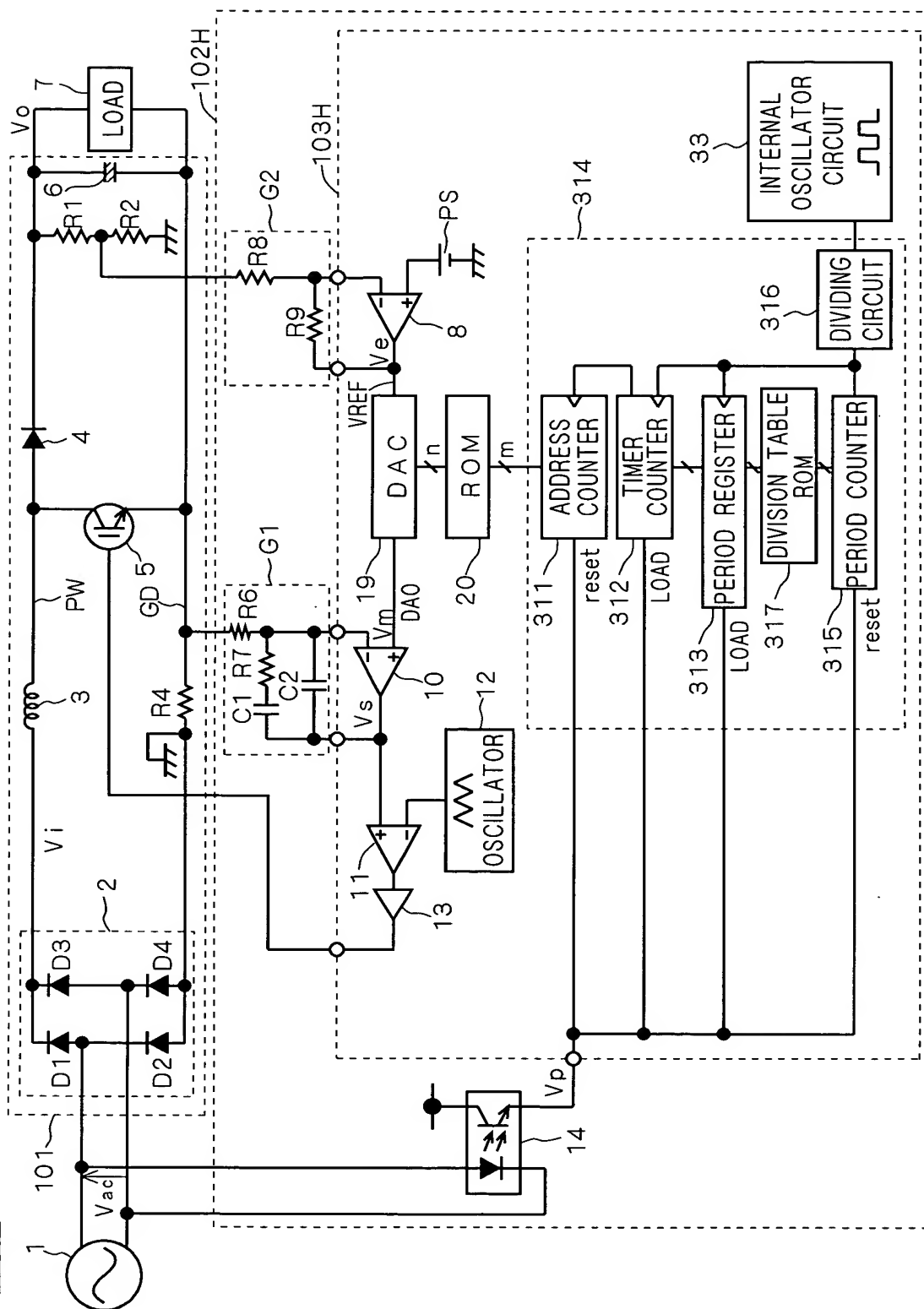
F I G . 1 7

| ROM ADDRESS	K 1 '
0	0
1	0
2	1
3	1
4	2
5	2
6	3
7	3
...	...

FIG.18
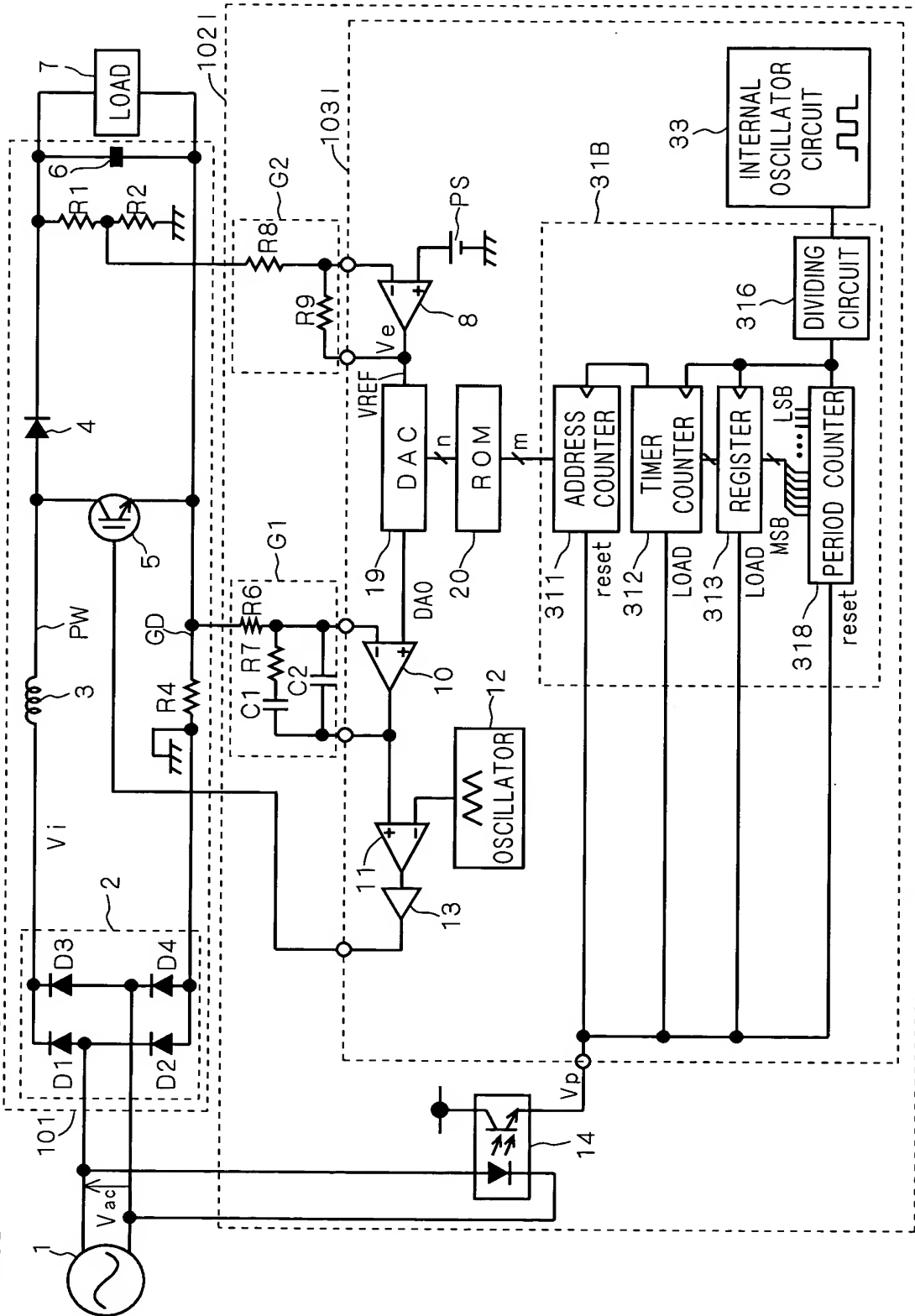
F I G . 1 9

		BINARY							DECIMAL
	MSB							LSB	
ORIGINAL DATA	0	1	1	1	0	0	0	0	→ 112
1-BIT RIGHT-SHIFT	0	0	1	1	1	0	0	0	→ 56
2-BIT RIGHT-SHIFT	0	0	0	1	1	1	0	0	→ 28